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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)

/Negussie Worku/

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Substitute for form 1449/PTO

Sheet 1

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Under the Paperwork Reduction Act of 1995 Repersons are required respond to a collection of information unless it contains a valid OMB control number Complete if Known Application Number 10/656281 Filing Date 8 September 2003 First Named Inventor Kia Silverbrook Art Unit 2625 Examiner Name Negussie Worku Attorney Docket Number BAL51US

			U. S. PATENT		
Examiner Initials*	No.1	Document Number Number-Kind Code ² (F #80000)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		4868676 A	Sep 19 1989	Matsuura et al	
		5847836 A	Dec 8 1998	Suzuki	
	_	5990469	Nov 23 1999	Bechtel et al	
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		FORE	IGN PATENT DOCU	MENTS		
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		Country Code ³ Number ⁴ Kind Code ⁹ (# known)	MM-DD-YYYY			
		WO 99/04368 A	Jan 28 1999	Silverbrook Research	Ptv Ltd	
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* Copies not attached as previously submitted to or cited by the USPTO in parent patent application No 6,618,117

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Substitu	te for form 1449/PTO			Complete If Known			
				Application Number	10/656281		
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where publisher	T²
		DUNSMORE CA et al. 7 A low-cost megapival digital camera using high-performance in-carnera image processing* Proceedings, IS&T's PICS Conference. Annual Conference, Proceedings of Conference for the Society for Imaging Science and Technology, 17 May 1589 (1986-05-17), pages 67-70, XPO(1012570 * figure 2 *	1
		PETIT L et al., "VLM processor architecture adapted to FPA's" Proceedings of the SPIE – The International Society for Optical Engineering SPIE-INT. SOC. OPT. ENG. USA, vol 3410, 1998, pages 128-132, XP0023S3310 ISSN: 0016-768X	1
		SUKEMIRA T., "FRS00 VLIW-Architecture High-performance Embedded Microprocessor" Fujitsu-Scientific and Technical Journal, Fujitsu Limited, Kawasaki, JP, Vol. 36, No.1, June 2000 (2000-06), pages 31-38, XP001106190 (SSN: 0016-225)	1
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